

Electronic Acknowledgement Receipt

EFS ID:	3166970		
Application Number:	10829539		
International Application Number:			
Confirmation Number:	7334		
Title of Invention:	Method and system for provisioning logical circuits for intermittent use in a data network		
First Named Inventor/Applicant Name:	William Taylor		
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Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$1050

RAM confirmation Number	8615				
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The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:					
Charge any Additional Fees required under 37 C.F.R. Section 1.21 (Miscellaneous fees and charges)					
File Listing:					
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part/.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	030294_0348US01_Amendment.pdf	121948 d8ace77cc31e6ec200ec37978301e9c78 641a12a9	no	21
Warnings:					
Information:					
2	Extension of Time	030294_0348US01_EOT.pdf	43034 3d148bc3c1e348b2c75705e053e7380 7e62315ab9	no	1
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8203 c079c7533dc099ee45246c4c352f6a0 bb022165	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				173185	
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